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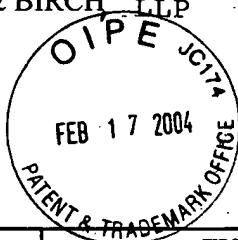
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2292 7590 11/17/2003

BIRCH STEWART KOLASCH & BIRCH, LLP  
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(D)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/054,823	01/25/2002	Hironori Matsumoto	0397-0439P	5766

TITLE OF INVENTION: INTEGRATED SEMICONDUCTOR CIRCUIT DEVICE HAVING SCHOTTKY BARRIER DIODE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	02/17/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
GREENE, PERSHELLE L	2826	257-471000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

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1 Birch, Stewart,

2 Kolasch & Birch, LLP

3 \_\_\_\_\_

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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Sharp Kabushiki Kaisha

Osaka, JAPAN

Please check the appropriate assignee category or categories (will not be printed on the patent):  individual  corporation or other private group entity  government

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(Authorized Signature) Charles Gorensstein (Date) 2/17/04  
Charles Gorensstein, #29,271

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02/20/2004 MBERHE1 00000244 10054823

01 FC:1501	1330.00	OP
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